Amendments to the Claims:

The following listing of claims will replace all prior versions, and listings, of claims in the application:

1. (Currently Amended) ~~An electronic circuit having a plurality of unit circuits,~~ An electronic circuit comprising:

a plurality of unit circuits;

a first power source line; and

a control circuit that sets a potential of the first power source line to a plurality of potentials or controls an electrical disconnection and an electrical connection between the first power source line and a predetermined voltage,

each of the plurality of unit circuits ~~comprising~~ including:

a first transistor having a first terminal, a second terminal, and a first control terminal;

a second transistor having a third terminal, a fourth terminal, and a second control terminal, ~~the third terminal being connected to the first control terminal;~~ the third terminal and the fourth terminal being coupled to the first control terminal and the first power source line, respectively;

a capacitive element having a first electrode and a second electrode, the first electrode being ~~connected~~ coupled to the first control terminal; and

a third transistor having a fifth terminal and a sixth terminal, ~~the fifth terminal being connected to the second electrode,~~ the third transistor controlling an electrical connection between a data line and the second electrode, and

an electrically conductive state of the first transistor being set during at least a part of a first period in which the data line is electrically connected to the second electrode through the third transistor.

~~the fourth terminal being connected to a first power source line in common with the fourth terminals of other unit circuits of the plurality of unit circuits, and~~

~~the electronic circuit further comprising a control circuit that sets a potential of the first power source line to a plurality of potentials or controls electrical disconnection and electrical connection between the first power source line and a driving voltage.~~

2. (Currently Amended) ~~An electronic circuit having a plurality of unit circuits, each of the plurality of unit circuits comprising:~~ The electronic circuit according to claim 1, further comprising a second power source line that is coupled to the second terminal.

~~a first transistor having a first terminal, a second terminal, and a first control terminal;~~

~~a second transistor having a third terminal, a fourth terminal, and a second control terminal, the third terminal being connected to the first control terminal;~~

~~a capacitive element having a first electrode and a second electrode, the first electrode being connected to the first control terminal; and~~

~~a third transistor having a fifth terminal and a sixth terminal, the fifth terminal being connected to the second electrode,~~

~~the fourth terminal being connected to a first power source line in common with the fourth terminals of other unit circuits of the plurality of unit circuits,~~

~~the second terminal being connected to a second power source line, and~~

~~the electronic circuit further comprising a control circuit that sets a potential of the first power source line to a plurality of potentials or controls electrical disconnection and electrical connection between the first power source line and a driving voltage.~~

3. (Currently Amended) The electronic circuit according to ~~Claim~~ claim 1, the second control terminal being ~~connected~~ coupled to the third terminal.

4. (Currently Amended) The electronic circuit according to ~~Claim~~ claim 1,

each unit circuit of the plurality of unit circuits ~~do~~ not ~~include~~ including any transistor other than the first transistor, the second transistor, and the third transistor.

5. (Currently Amended) The electronic circuit according to ~~Claim~~ claim 1, ~~the conductive types of the first transistor and the second transistor being equal to each other.~~ a conduction type of the first transistor being identical with a conduction type of the second transistor.

6. (Currently Amended) The electronic circuit according to ~~Claim~~ claim 1, ~~an electronic element being connected to the first terminal.~~each of the plurality of unit circuits further including an electronic element coupled to the first terminal.

7. (Currently Amended) The electronic circuit according to ~~Claim~~ claim 6, the electronic element being a current-driven element.

8. (Currently Amended) The electronic circuit according to ~~Claim~~ claim 1, the control circuit being a fourth transistor having a seventh terminal and an eighth terminal.~~, and~~ ~~the seventh terminal being connected to the fourth terminal through the first power source line, and the eighth terminal is connected to the driving voltage.~~

9. (Currently Amended) The electronic circuit according to ~~Claim 1~~ claim 2, the second power source line ~~also being electrically connected to the driving~~ being set to the predetermined voltage.

10. (Currently Amended) The electronic circuit according to ~~Claim~~ claim 1, a threshold voltage of the first transistor not being ~~set to be not~~ lower than a threshold voltage of the second transistor.

11. (Currently Amended) An electronic circuit ~~having~~ comprising:

a plurality of first signal lines~~,~~;

a plurality of second signal lines~~,~~;

a plurality of first power source lines~~;~~;

a control circuit that sets a potential of each of the plurality of first power sournce lines to a plurality of potentials or controls an electrical disconnection and an electrical connection between one first power source line of the plurality of first power sournce lines and a predetermined voltage; and

~~and~~ a plurality of unit circuits, each of the plurality of unit circuits ~~comprising~~ including:

a first transistor having a first terminal, a second terminal, and a first control terminal;

a second transistor having a third terminal, a fourth terminal, and a second control terminal, the third terminal and the fourth terminal being ~~connected~~ coupled to the first control terminal and one power source line of the plurality of power source lines, respectively;

a capacitive element having a first electrode and a second electrode, the first electrode being ~~connected~~ coupled to the first control terminal; and

a third transistor having a fifth terminal, ~~and~~ a sixth terminal, and a third control terminal, the third transistor controlling an electrical connection between the second electrode and one second signal line of the plurality of second signal lines, the third control terminal being coupled to one first signal line of the plurality of first signal lines, and ~~the fifth terminal being connected to the second electrode,~~

an electrically conductive state of the first transistor being determined during at least a part of a period in which the one second signal line is electrically connected to the second electrode through the third transistor.

~~the second control terminal being connected to the third terminal, and~~

~~the third control terminal being connected to a corresponding first signal line of~~

~~the plurality of first signal lines.~~

12. (Currently Amended) ~~An~~ The electronic circuit according to ~~Claim~~ claim 11, further comprising a plurality of second power source lines one of which is coupled to the second terminal. ~~the fourth terminal being connected to a first power source line in common with the fourth terminals of other unit circuits of the plurality of unit circuits,~~

~~the second terminal being connected to a second power source line, and~~

~~the electronic circuit further comprising a control circuit that sets a potential of the first power source line to a plurality of potentials or controls electrical disconnection and electrical connection between the first power source line and a driving voltage.~~

13. (Currently Amended) The electronic circuit according to ~~Claim~~ claim 11,

a conduction type ~~the conductive types~~ of the first transistor being identical with a conduction type of the ~~and the~~ second transistor. ~~being equal to each other.~~

14. (Currently Amended) The electronic circuit according to ~~Claim~~ claim 11,

each of the plurality of unit circuits further including an electronic element ~~being connected~~ that is coupled to the first terminal.

15. (Currently Amended) An electronic circuit comprising:

a first signal line;

a second signal line;

a power source line; and

a plurality of unit circuits, each of the plurality of unit circuits including:

a first transistor having a first terminal, a second terminal, and a first control terminal;

a second transistor having a third terminal that is coupled to the first control terminal and a fourth terminal that is directly connected to the power source line;

a capacitive element having a first electrode that is coupled to the first

control terminal and a second electrode; and

a third transistor having a fifth terminal that is coupled to the second electrode, a sixth terminal that is coupled to the second signal line, and a third control terminal that is coupled to the first signal line, and

the first electrode being electrically connected to the power source line through the second transistor during a first period before a second period in which the data signal is transmitted to the capacitive element through the third transistor.

~~having a plurality of unit circuits, each of the plurality of unit circuits comprising:~~

~~a holding element that holds a signal as charge;~~

~~a switching transistor that controls transmission of the signal to the holding element;~~

~~a driving transistor in which an electrically conductive state is set on the basis of the charge held in the holding element; and~~

~~an adjusting transistor that sets a control terminal of the driving transistor to a predetermined potential before the transmission of the signal to the holding element,~~

~~the electronic circuit further comprising a control circuit that supplies a driving voltage to the adjusting transistors of at least two unit circuits of the plurality of unit circuits.~~

16. (Currently Amended) The electronic circuit according to ~~Claim~~ claim 15,

each of the plurality of unit circuits further including an electronic element ~~being~~ that is ~~connected~~ coupled to the driving transistor.

17. (Currently Amended) The electronic circuit according to claim 15, further comprising:

a control circuit that sets a potential of the power source line to a plurality of potentials or controls an electrical disconnection and an electrical connection between the power source line and a predetermined voltage.

~~A method of driving an electronic circuit having a plurality of unit circuits, each of the plurality of unit circuits comprising:~~

~~a first transistor having a first terminal, a second terminal, and a first control terminal;~~

~~a second transistor having a third terminal and a fourth terminal, the third terminal being coupled to the first control terminal; and~~

~~a capacitive element having a first electrode and a second electrode, the first electrode being coupled to the first control terminal,~~

~~the method comprising:~~

~~a first step of electrically connecting the respective third terminals of the plurality of unit circuits to a predetermined potential and setting the first control terminals to a first potential; and~~

~~a second step of varying a potential of the first control terminals from the first potential, by varying a potential of the second electrodes from a second potential to a third potential in a state in which the third terminals are electrically disconnected from the predetermined potential.~~

18. (Currently Amended) The electronic circuit according to claim 15,

each unit circuit of the plurality of unit circuits not including any transistor other than the first transistor, the second transistor, and the third transistor.

~~The method of driving an electronic circuit according to Claim 17, at least for a time required to carry out the first step, the method being carried out in a state in which the potential of the second electrode is set to the second potential.~~

19. (Currently Amended) An electro-optical device, the device comprising:

~~having~~ a plurality of data lines~~,~~;

a plurality of scanning lines~~,~~;

a plurality of first power source lines;

a control circuit that sets each of the plurality of first power source lines to a plurality of potentials or that controls electrical connection and disconnection between each of the plurality of first power source lines and a predetermined voltage; and

~~and~~ a plurality of unit circuits, each of the plurality of unit circuits ~~comprising~~ including:

a first transistor having a first terminal, a second terminal, and a first control terminal;

an electro-optical element ~~being~~ that is coupled to the first terminal;

a second transistor having a third terminal and a fourth terminal that are coupled to the first control terminal and one first power source line of the plurality of first power source lines; ~~the third terminal being coupled to the first control terminal;~~

a capacitive element having a first electrode that is coupled to the first control terminal, and a second electrode ~~the first electrode being coupled to the first control terminal~~; and

a third transistor having a fifth terminal that is coupled to the second electrode, a sixth terminal that is coupled to one data line of the plurality of data lines, and a third control terminal that is coupled to one scanning line of the plurality of scanning lines, and

an electrically conductive state of the first transistor being determined during at least a part of a period in which the one data line is electrically connected to the second electrode through the third transistor. ~~the fifth terminal being electrically coupled to the second electrode,~~

~~the fourth terminal being connected to a first power source line in common with the fourth terminals of other unit circuits of the plurality of unit circuits,~~

~~the third control terminal being connected to a corresponding scanning line of~~

~~the plurality of scanning lines,~~

~~the sixth terminal being connected to a corresponding data line of the plurality of data lines, and~~

~~the electro-optical device further comprising a control circuit that sets a potential of the first power source line to a plurality of potentials or controls electrical disconnection and electrical connection between the first power source line and a driving voltage.~~

20. (Currently Amended) ~~An~~ The electro-optical device according to claim 19, further comprising a plurality of second power source lines, and

the second terminal being coupled to one second power source line of the plurality of second power source lines. ~~having a plurality of data lines, a plurality of scanning lines, and a plurality of unit circuits, each of the plurality of unit circuits comprising:~~

~~a first transistor having a first terminal, a second terminal, and a first control terminal;~~

~~an electro-optical element being connected to the first terminal;~~

~~a second transistor having a third terminal and a fourth terminal, the third terminal being coupled to the first control terminal;~~

~~a capacitive element having a first electrode and a second electrode, the first electrode being coupled to the first control terminal; and~~

~~a third transistor having a fifth terminal, a sixth terminal, and a third control terminal, the fifth terminal being coupled to the second electrode,~~

~~the fourth terminal being coupled to a first power source line in common with the fourth terminals of other unit circuits of the plurality of unit circuits,~~

~~the second terminal being coupled to a second power source line in common with the second terminals of other unit circuits of the plurality of unit circuits,~~

~~the third control terminal being coupled to a corresponding scanning line of the plurality of scanning lines,~~

~~the sixth terminal being coupled to a corresponding data line of the plurality of data lines, and~~

~~the electro-optical device further comprising a control circuit that sets a potential of the first power source line to a plurality of potentials or controls electrical disconnection and electrical connection between the first power source line and a driving voltage.~~

21. (Currently Amended) The electro-optical device according to ~~Claim~~ claim 19,

the second control terminal being ~~connected~~ coupled to the third terminal.

22. (Currently Amended) The electro-optical device according to ~~Claim~~ claim 19,

the control circuit being a fourth transistor having a seventh terminal and an eighth terminal, ~~and~~

the seventh terminal being coupled to the fourth terminal through ~~the~~ one first power source line of the plurality of first power source lines, and

the eighth terminal being coupled to the ~~driving~~ predetermined voltage.

23. (Currently Amended) The electro-optical device according to ~~Claim~~ claim 19,

each unit circuit of the plurality of unit circuits ~~do~~ not ~~include~~ including any transistor other than the first transistor, the second transistor, and the third transistor.

24. (Currently Amended) The electro-optical device according to ~~Claim~~ claim 19,

~~the conductive types~~ a conduction type of the first transistor being identical with a conduction type of ~~and~~ the second transistor ~~being equal to each other~~.

25. (Currently Amended) The electro-optical device according to ~~Claim~~ claim 19, a threshold voltage of the first transistor ~~being set to be~~ not being lower than a threshold voltage of the second transistor.

26. (Currently Amended) The electro-optical device according to ~~Claim~~ claim 20~~19~~,

the one second power source line ~~also~~ being ~~electrically~~ coupled to the ~~driving~~ predetermined voltage.

27. (Currently Amended) The electro-optical device according to ~~Claim~~ claim 19, the electro-optical element being an EL element.

28. (Currently Amended) The electro-optical device according to ~~Claim~~ claim 19,

the plurality of unit circuits including a group of unit circuits that arranged along one scanning line of the plurality of scanning lines,

the group of unit circuits being used for exhibiting the same color. ~~the electro-optical elements of the same color being arranged along the scanning lines.~~

29-31 (Canceled)

32. (Currently Amended) An electronic apparatus being equipped with the electronic circuit according to ~~Claim~~ claim 1.

33. (Currently Amended) An electronic apparatus being equipped with the electro-optical device according to ~~Claim~~ claim 19.

34. (New) The electro-optical device according to claim 19,

the plurality of first power source lines intersecting the plurality of data lines.

35. (New) The electro-optical device according to claim 19,

the plurality of first power source lines being arranged along the plurality of scanning lines.

36. (New) An electronic circuit comprising:

a plurality of unit ciruits;

a first power source line; and

a control circuit that sets a potential of the first power source line to a plurality of potentials or controls an electrical disconnection and an electrical connection between the first power source line and a predetermined voltage,

each of the plurality of unit circuits including:

a first transistor having a first terminal, a second transistor, and a first control terminal;

a second transistor having a third terminal, a fourth terminal, and a second control terminal, the third terminal and the fourth terminal being directly connected to the first control terminal and the first power source line, respectively;

a capacitive element having a first electrode and a second electrode, the first electrode being coupled to the first control terminal; and

a third transistor having a fifth terminal and a sixth terminal, the third transistor controlling an electrical connection between a data line and the second electrode.

37. (New) An electro-optical device, the device comprising:

a plurality of data lines;

a plurality of scanning lines;

a plurality of first power source lines; and

a plurality of unit circuits, each of the plurality of unit circuits including:

a first transistor having a first terminal, a second terminal, and a first control terminal;

an electro-optical element that is coupled to the first terminal;

a second transistor having a third terminal that is coupled to the first control terminal and a fourth terminal that is directly connected to one first power source line of the plurality of first power source lines;

a capacitive element having a first electrode that is coupled to the first

control terminal, and a second electrode; and

a third transistor having a fifth terminal that is coupled to the second electrode, a sixth terminal that is coupled to one data line of the plurality of data lines, and a third control terminal that is coupled to one scanning line of the plurality of scanning lines, and

the first electrode being electrically connected to the one power source line through the second transistor during a first period before a second period in which the data signal is transmitted to the capacitive element through the third transistor.

38. (New) The electronic circuit according to claim 6,

a driving voltage and a driving current whose levels correspond to the electrically conductive state of the first transistor are supplied to the electronic element during a second period.

39. (New) The electronic circuit according to claim 11,

the plurality of first power source lines intersecting the plurality of second signal lines.